

JAN 31 2006

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
YAMAMOTO et al.) Art Unit: 2815
)
Serial No. 10/805,822) Examiner: Wojciechowicz, Edward
)
Filed: March 22, 2004)
)
For: **Method of Fabricating Semiconductor**) Attorney Docket No.: 44471/298721
Integrated Circuit)

CERTIFICATE OF TRANSMISSION UNDER 37 CFR 1.8
I hereby certify that this correspondence is being transmitted to the
United States Patent Office Centralized Facsimile Number
(571.273.8300), on January 31, 2006.


Maggie Gately

Date: January 31, 2006

RESPONSE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Office action dated October 20, 2005 in the patent application identified above, please enter the following amendments and reconsider the application in view of the appended remarks. A request for a one-month extension of time accompanies this response.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.